1. Consider the full adder with inputs $a$, $b$, $ci$ and outputs $co$ and $s$.
   
a) Show the waveforms to perform the functional testing of the circuit.

b) Draw the waveforms to perform the delay testing of the circuit. Indicate where the delays should be measured.

2. Consider the flip-flop with synchronous set. The inputs are $d$, $clk$, and $set$. The output is $q$.
   
a) Show the waveforms to perform the functional testing of the circuit.

b) Draw the waveforms to perform the delay testing of the circuit. Indicate where the delays should be measured.

Homework assignments are in the Class Folder and on the web page, http://jan.ucc.nau.edu/~ejb3/ee482